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FIG. 2A: 11/09/06  
REVISION SHEET SHOWING CHANGES

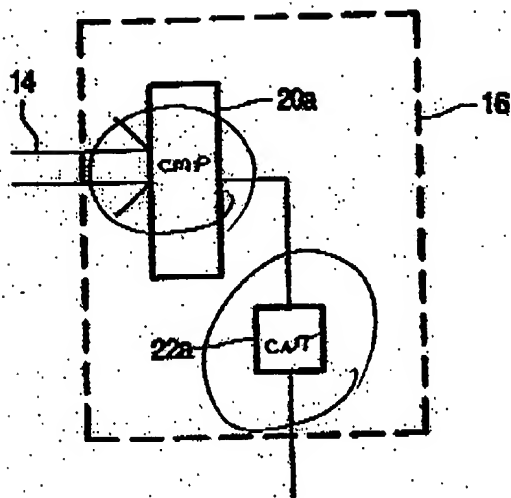


FIG. 2

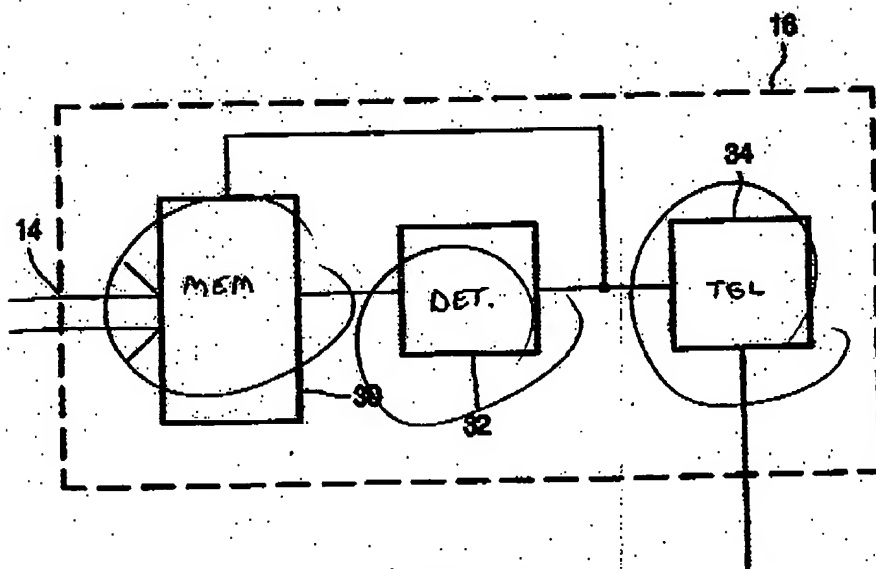


FIG. 3

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FIG. 3a is a block diagram of a system for detecting repetition of a signal. The system includes a first repetition detector (300a) and a second repetition detector (300b). The first repetition detector (300a) is connected to a status comparator (302). The second repetition detector (300b) is also connected to the status comparator (302). The status comparator (302) is connected to a control register (304). The control register (304) is connected to a bus (16).

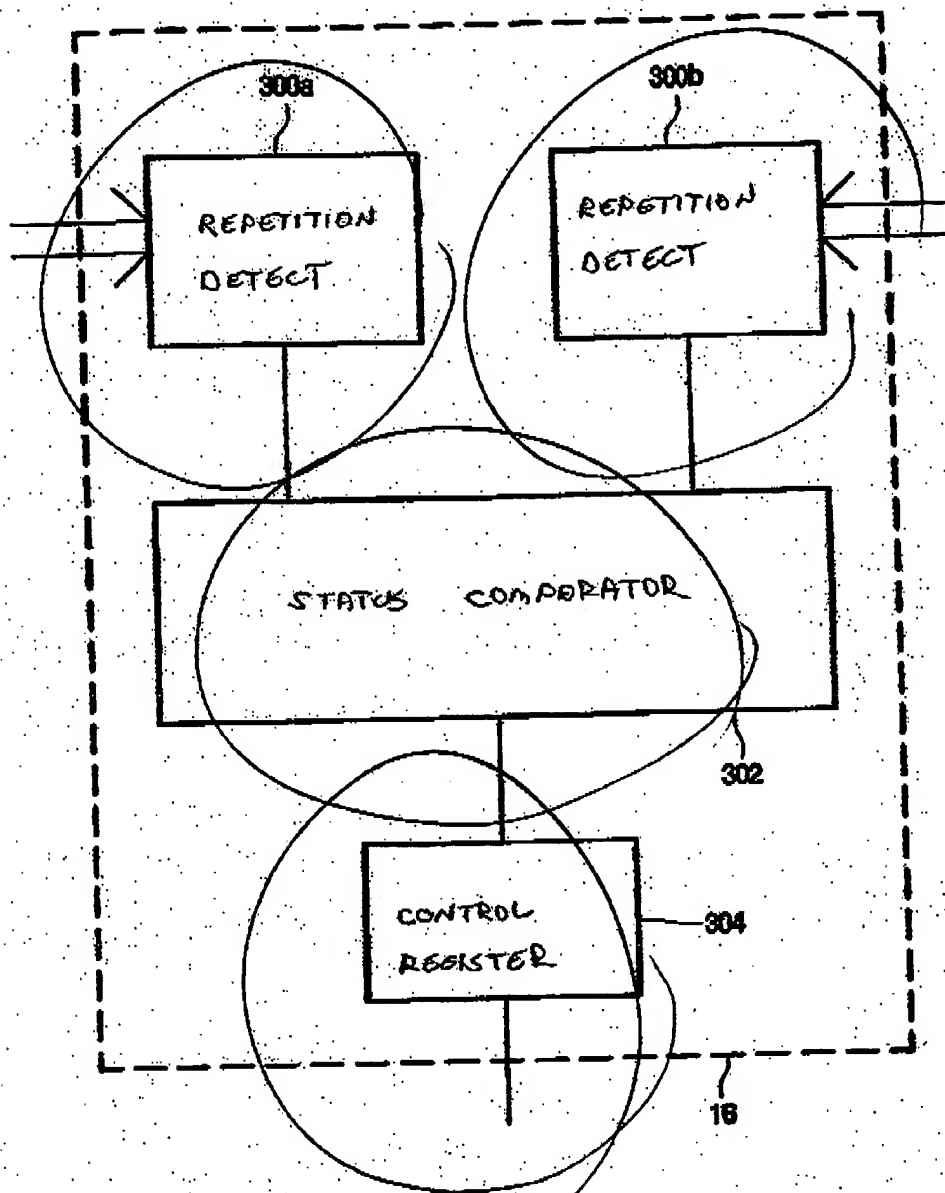


FIG. 3a

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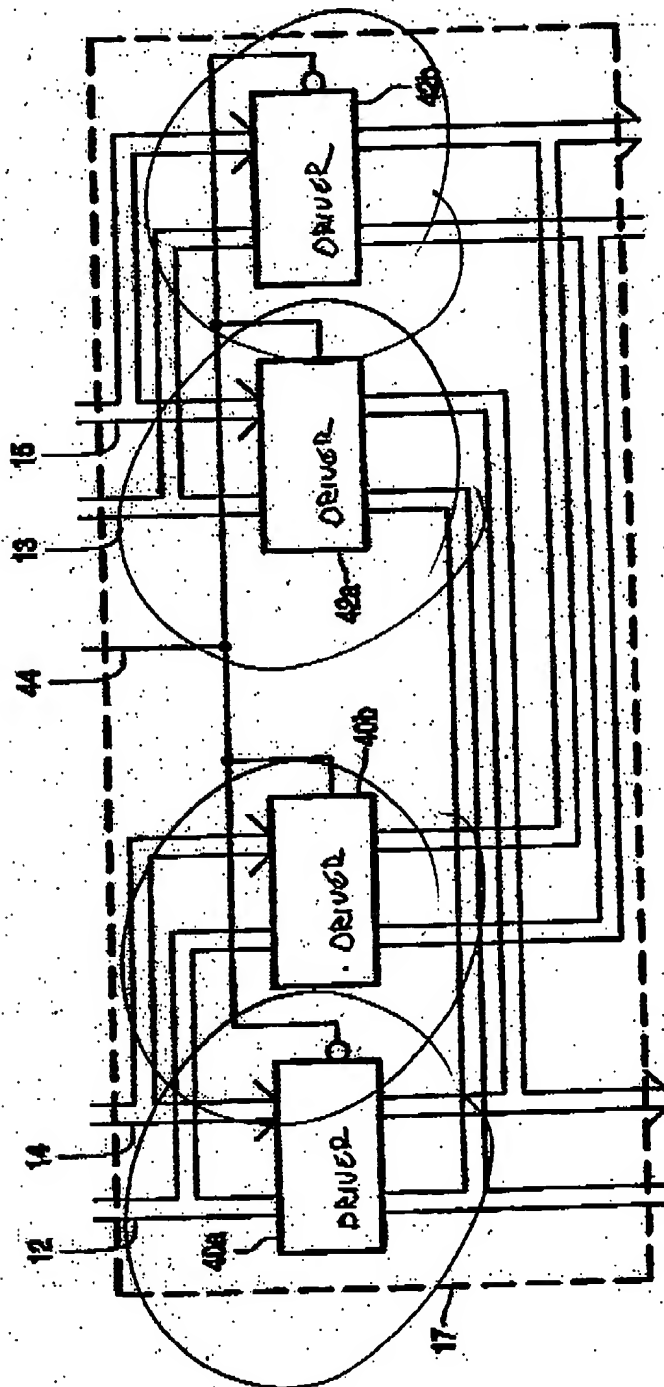


FIG. 4

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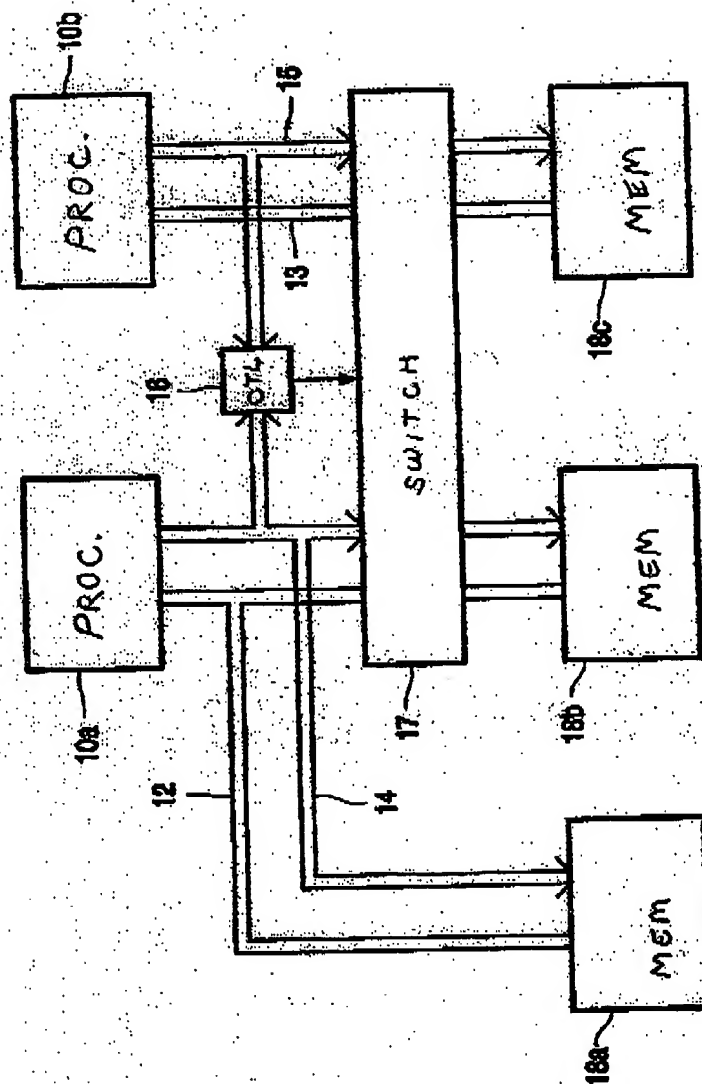


FIG. 1

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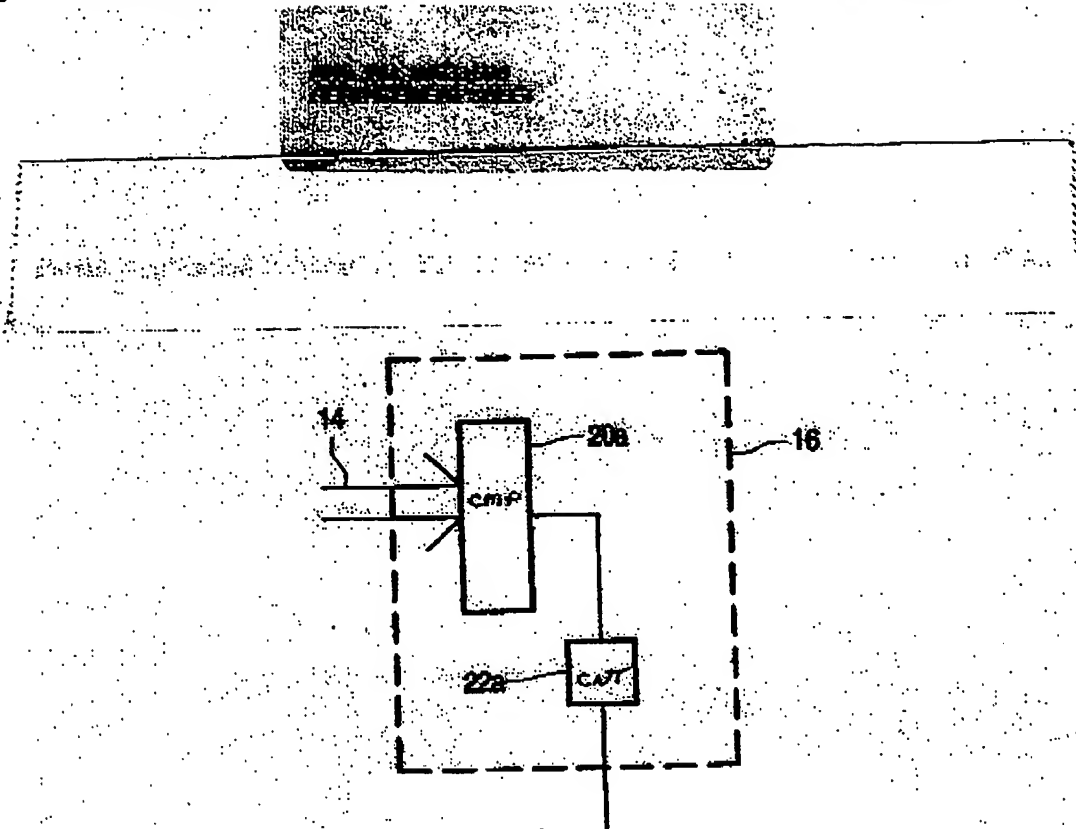


FIG. 2

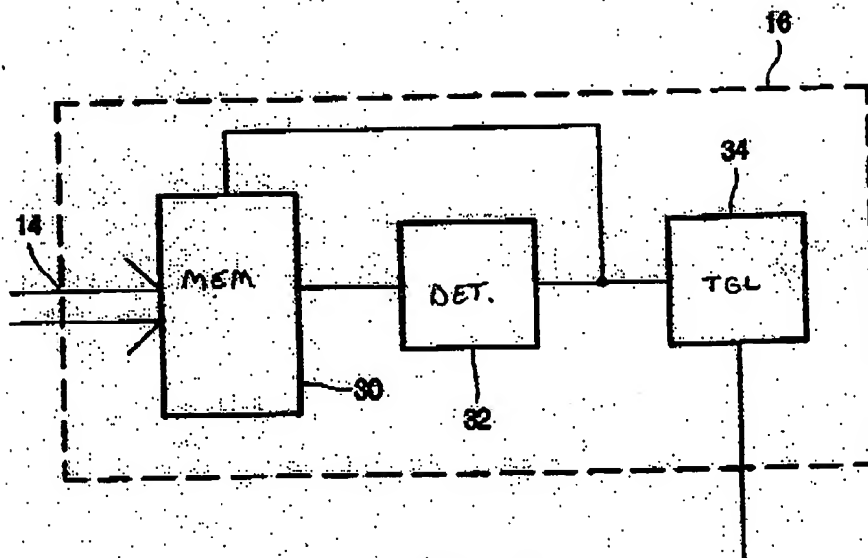


FIG. 3

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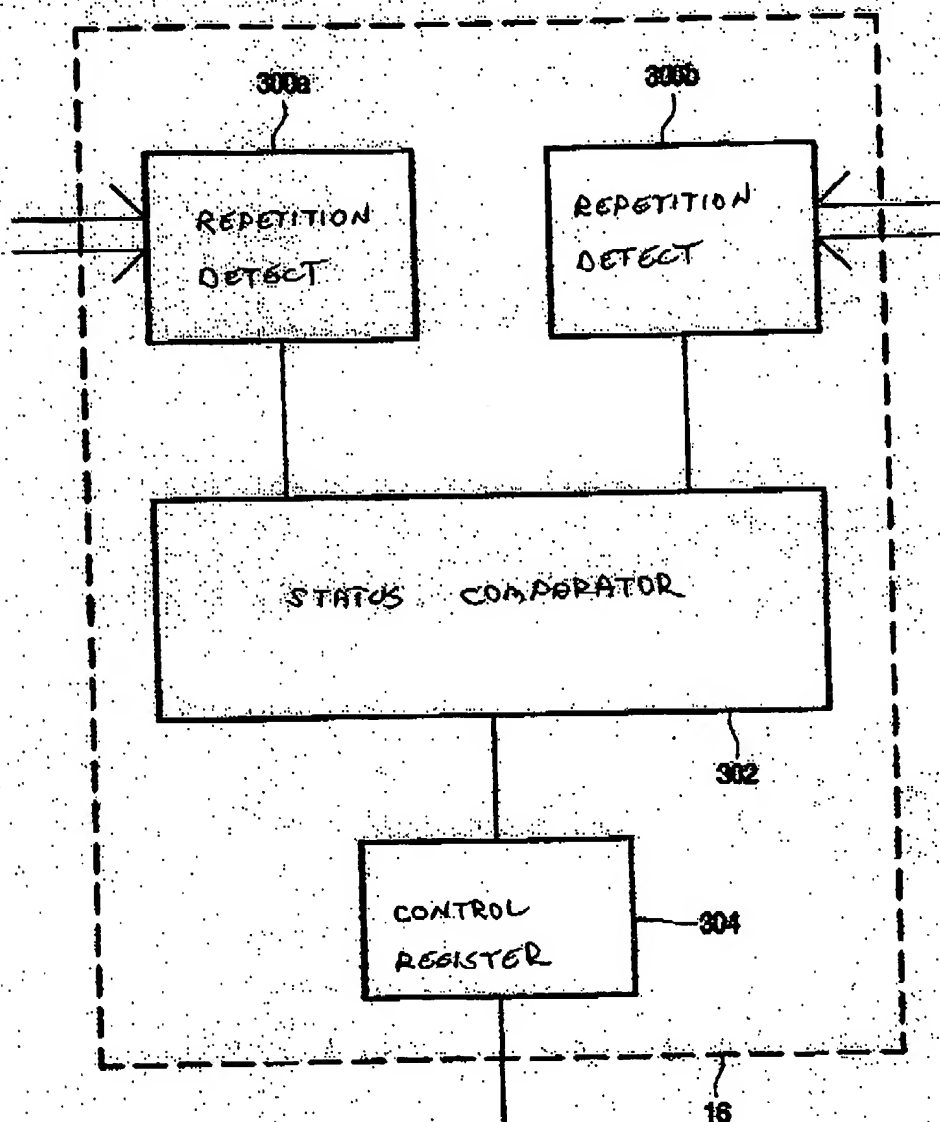


FIG. 3a

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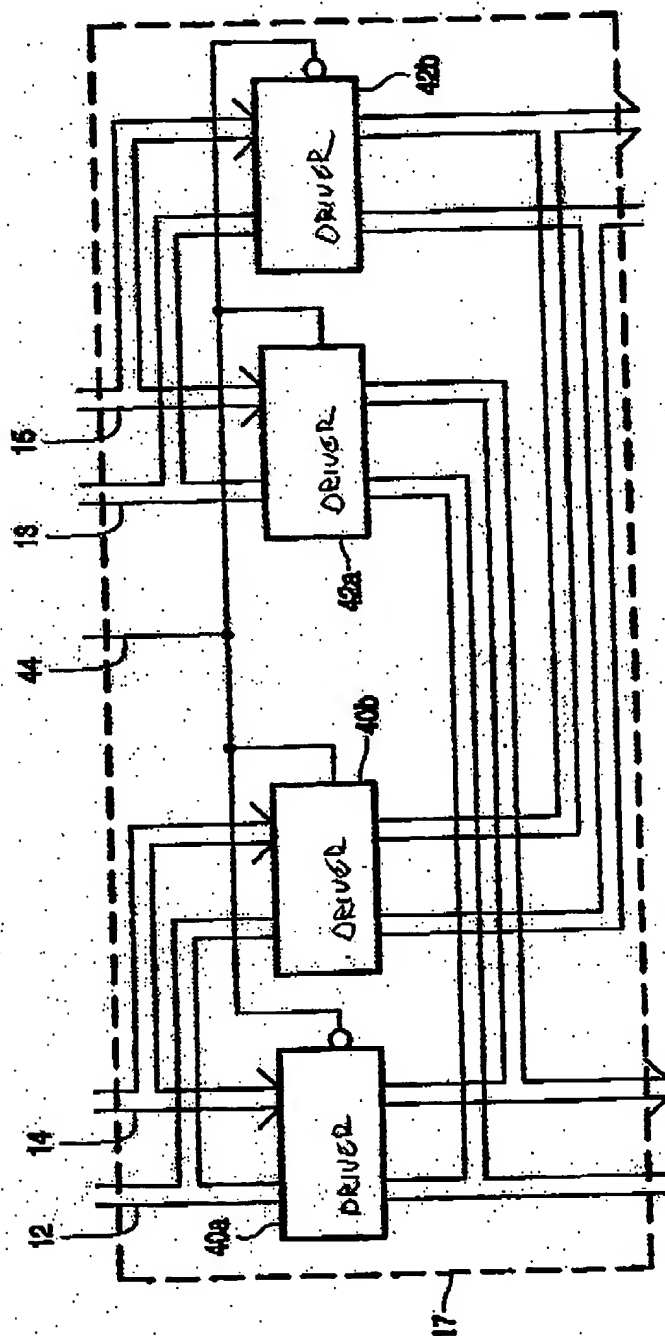


FIG. 4

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